

COURSE COMPLETION CERTIFICATE

The certificate is awarded to

Vignesh

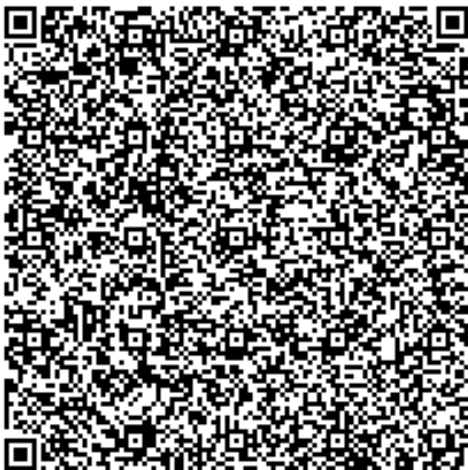
for successfully completing the course

VLSI Digital Design - Chip Design and Verilog Programming

on April 17, 2025

Infosys | Springboard

Congratulations! You make us proud!



Issued on: Monday, April 21, 2025
To verify, scan the QR code at <https://verify.onwingspan.com>

A handwritten signature in black ink, belonging to Thirumala Arohi.

Thirumala Arohi
Executive Vice President and Global Head
Education, Training & Assessment (ETA)
Infosys Limited