

COURSE COMPLETION CERTIFICATE

The certificate is awarded to

Harshala .V.

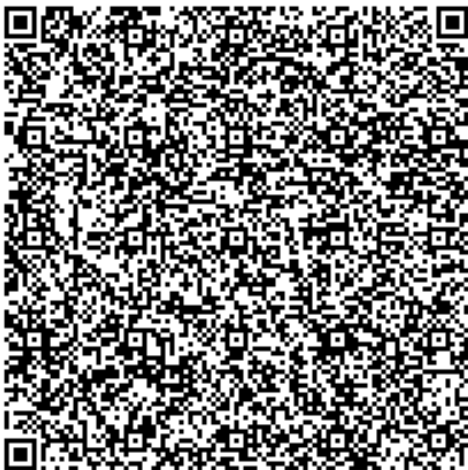
for successfully completing the course

VLSI Digital Design - Chip Design and Verilog Programming

on January 26, 2025

Infosys | Springboard

Congratulations! You make us proud!



Issued on: Wednesday, January 29, 2025
To verify, scan the QR code at <https://verify.onwingspan.com>

A handwritten signature in black ink, belonging to Thirumala Arohi.

Thirumala Arohi
Executive Vice President and Global Head
Education, Training & Assessment (ETA)
Infosys Limited